Electronic Acknowledgement Receipt				
EFS ID:	3218624			
Application Number:	10512017			
International Application Number:				
Confirmation Number:	8467			
Title of Invention:	Semiconductor component with integrated capacitance structure and method for fabrication thereof			
First Named Inventor/Applicant Name:	Thomas Benetik			
Customer Number:	48581			
Filer:	John G. Rauch/Olivia Lopez			
Filer Authorized By:	John G. Rauch			
Attorney Docket Number:	10808/169			
Receipt Date:	28-APR-2008			
Filing Date:	29-APR-2005			
Time Stamp:	17:07:12			
Application Type:	U.S. National Stage under 35 USC 371			
Payment information:				
Submitted with Payment	yes			
Payment Type	Deposit Account			
Payment was suggestfully received in DAM	\$220			

Submitted with Payment	yes	
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Payment was successfully received in RAM	\$930	
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Warnings:	<u>.</u>	<u>-</u>			
Information:					
2 Extension of Time	10808169EXTOFTIME.pdf	73853	no	2	
-	2 Extension of Time	10808169EXTOFTIME.pdi	b4b6b6a76321a5012±639c9d441ff435 1cf64cb9	no	2
Warnings:					
Information:					
Request for Continued Examination (RCE)	10808169RCE.pdf	96638	no	2	
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Warnings:					
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4 Fee Worksheet (PTO-06)	fee-info.pdf	8377	no	2	
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